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Representing the DØ Collaboration

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ABSTRACT

We have studied the performance characteristics of approximately 4000 Visible Light Photon Counters(VLPC) over a range of operating temperatures and biases. We will describe the characterization procedure and present the measured gain, noise, and relative quantum efficiency for the devices. We will also discuss the general operation of the devices and the significant problems encountered during the characterization.

1. Introduction

In 1997, DØ will upgrade the central tracking region of the detector with an entirely new system consisting of a 2 Tesla superconducting solenoid, a 6000 channel preshower detector made of scintillator with wavelength shifting fiber readout, an outer 80000 channel scintillating fiber tracker and an inner 10⁶ channel silicon strip tracker. The optical signal from the fibers will be detected by Visible Light Photon Counters(VLPCs). VLPCs are arsenic doped silicon diodes that operate near a temperature of 6.5 K with a bias of approximately 6.5 V.2 The detailed examination of the characteristics of the VLPCs is the subject of this report.

2. Characterization Stand

2.1. Characterization Procedure

The VLPC chips are produced by Rockwell and are supplied as 1×8 pixel arrays with individual pixels of 1 mm diameter. The arrays are mounted on a ceramic substrate to form a hybrid and this hybrid is then mounted on a Torlon carrier that allows for fiber alignment with the pixels. Noise, gain, and quantum efficiency (QE) measurements were taken on all pixels at operating points between 5.5 and 7.0 K

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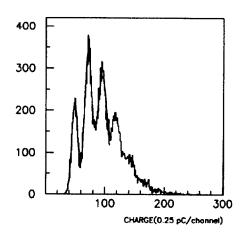


Fig. 1. A typical spectrum from a good VLPC pixel at T=6.5 K and V=6.5 V.

and with VLPC bias between 5.9 and 7.1 V. An LED light pulser was used to inject an optical signal into the devices. Pulser data along with pedestal data were used to calculate noise, gain, and relative QE.

2.2. Systematic Errors

The combined systematic errors on the gain due to the run to run change of cassette condition, the channel to channel variation of preamp gain and the channel to channel VLPC bias variation is about 5%.

The channel to channel light level variation is estimated by cycling a hybrid (calibration chip) through each of the positions in the cassette and measuring the response. The systematic error on QE measurement from light level variation is about 5%.

3. Results

Figure 1 shows a typical spectra obtained from a good pixel. One sees well separated pedestal, first and second photoelectron peaks.

3.1. Dark Current

Dark current was measured by fitting the pedestal spectra to a Gaussian distribution. The extra counts above the tail were considered to be dark current from single photoelectron events. The rates shown in Fig. 2 are essentially the singles rates at a threshold of 0.5 photoelectrons.

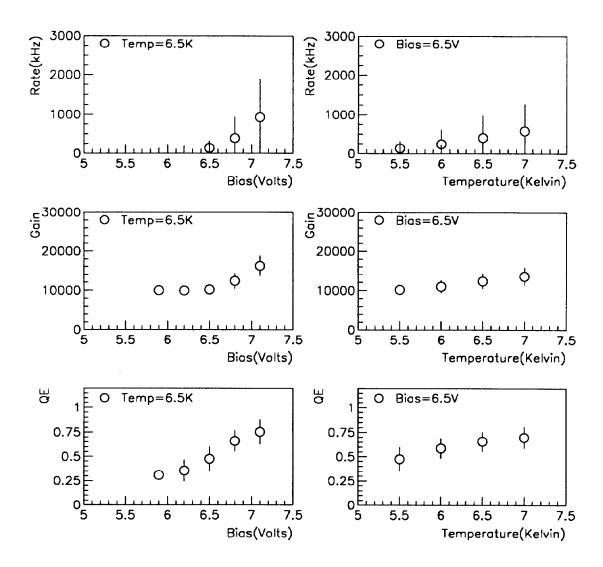


Fig. 2. Dark current, Gain and QE as a function of VLPC bias at T=6.5 K and as a function of operating temperature at V=6.5 V.

3.2. Gain

The gain is defined as the difference between the first and second photoelectron peak positions. At our nominal operating point of 6.5 V and 6.5 K, the gain = $12.6 \times 10^4 \pm 2500$.

3.3. Quantum Efficiency

The relative QE is obtained by comparing the average number of photoelectrons observed from pixel to pixel and normalizing the distribution to an average QE of 60% at 6.5 K and 6.5 V.

3.4. Yield

520 hybrids have been characterized. Of those characterized, 320 were considered good at the test stand by looking at spectra from the T=6.5 K and V=6.5 V point, 120 had at least 1 bad pixel, and 80 were considered marginal and were retested. Of the marginal chips re-tested, 34 were recovered as good chips. The recovery can be attributed to better contact with the read-out springs in the cassette.

4. Acknowledgements

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References

- 1. E823 ($D\emptyset$ Upgrade): $D\emptyset_{\beta}$, $D\emptyset$ note 1733 (1993)
- 2. M. D. Petrov, M. G. Stapelbroek, and W. Z. Kleinhans, Appl. Phys. Lett. 51, 406 (1987).